

Abstracts

A MESFET Model for Use in the Design of GaAs Integrated Circuits

W.R. Curtice. "A MESFET Model for Use in the Design of GaAs Integrated Circuits." 1980 Transactions on Microwave Theory and Techniques 28.5 (May 1980 [T-MTT] (Special Issue on Gigabit Logic for Microwave Systems)): 448-456.

A MESFET model is presented that is suitable for use in conventional, time-domain circuit simulation programs. The parameters of the model are evaluated either from experimental data or from more detailed device analysis. The model is shown to be more complete than earlier models, which neglect transit-time and other effects. An integrated circuit (IC) design example is discussed.

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